

CHAITRA R NANJANGUD

#121, Gokula 1st stage, 1st phase
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EDUCATION

- M Tech in Digital Electronics with FCD from Sri Dharmasthala College of Engineering, Dharwad, Karnataka, India. Completed in 2011.

Thesis: LPC and Wavelet based feature extraction for speech recognition.

In this project, new feature extraction methods, which utilize wavelet decomposition and reduced order linear predictive coding (LPC) coefficients, have been proposed for speech recognition. The coefficients have been derived from the speech frames decomposed using discrete wavelet transform. LPC coefficients derived from sub band decomposition of speech frame provide better representation than modeling the frame directly. The WLPC coefficients have been further normalized in cepstrum domain to get new set of features denoted as wavelet sub band cepstral mean normalized features. The proposed approaches provide effective (better recognition rate), efficient (reduced feature vector dimension), and noise robust features. The performance of these techniques have been evaluated on the TI-46 isolated word database for numerical digits from 0 to 9 in a white noise environment using the continuous density hidden Markov model.

- B.E in Electrical and Electronics with FC from B. V. Bhommaraddi College of Engineering, Hubli, Karnataka, India. Completed in 2008.

Thesis: DTMF based Home security system.

Generally, appliances used in our home are controlled with the help of switches. These days, you can see automation of these appliances using many technologies. This project presents the controlling of home appliances using DTMF technology.

COMPUTER PROFICIENCY

Using Windows, Linux operating systems and MS Office. Circuit Simulations using P Spice, Matlab. 8051 microcontroller simulation using Kiel, VLSI Circuit design using Cadence, Electrical CAD. MSP 430 Microcontroller simulations using Code Composer Studio.

AREAS OF INTEREST

Signals and Systems, Digital Signal processing, Microcontrollers, Logic Design, Analog Electronic circuits

STRENGTHS

- Effective Communication, Positive Mental Attitude, Time Consciousness, Capacity to work as a Team.
 - A Quick & fast learner.
 - Good Communication Skills and analytical skills.
 - Flexible - can adjust to new situations very quickly.
 - Capability of handling complex situations and working under pressure.
 - Demonstrated ability of team handling skills and team spirit.
 - Friendly and self motivated and keen to maintain the morale always at high levels.
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PAPER PRESENTATIONS/WORKSHOPS/SEMINAR

- "Analog and Mixed signal Designs", BVBCET, Hubli, July 2009.
 - "Automatic Speech Recognition", Research and Training Unit for navigational Electronics, Osmania University Hyderabad, 6-9 September 2010
 - Presented the paper "LPC and Wavelet Based Feature Extraction for speech Recognition" R.V.College of Engineering, Bangalore, Karnataka on 17th May 2011.
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NATURE OF JOB

Handling theory and Practical classes for undergraduate students, other academic works.

Theory subjects handled: Microcontrollers, Signals and systems, Analog Electronic Circuits, Logic Design, Control Systems, Electrical Engineering Materials, Management, Switch Gear and Protection, Electronic Instrumentation, Testing and Commissioning.

Labs handled: Analog Electronic Circuits lab, Logic Design Lab and Microcontroller Labs

PERSONAL DETAILS

Date of Birth	16 May 1985
Gender	Female
Fathers name	Raghavendra
Nationality	Indian
Languages known	English, Kannada, Hindi
