


Curriculum – Vitae

1.	Name	VENKATESHAPPA				
						
2.	Address and Contact Numbers	193/4,S1,ONE X HOME.INDIAN EXPRESS LAY OUT,VIDYARANYAPURA POST BANGALORE- 560097				
3.	Date of Birth/Sex/Marital Status	18/12/1971/MALE/MARRIED				
4.	Affiliation	Asso Prof and HOD ECE , M S Engineering College				
5.	Educational Qualification	Degree	year	University/Coll ege	Specialization	
		(P.hd)	2013	VTU	VLSI&IMAGE PROCESSING	
		M.Tech	2009	VTU/Sir,MVIT	ELECTRONICS	
		ME	2005	BU/UVCE	POWER ELECTRONICS	
		BE	1997	BU/DSCE	ELECTRONICS	
6.	Experience		College	From	To	Years
		1	MSEC	13FEB 2006	Till Date	9
		2	SRSIT	March 2000	Feb 2006	6
		3	SRI VIT	Aug98	Nov99	1
7.	Subject Areas of Interest	PE&VLSI				
8.	Research Projects Worked and Grants Received					
9.	Consultancy Projects Worked					
10.	Number of Projects/Thesis Supervised • Masters • PhD	04				
11.	Publications and Patents (Details can be added in annexure)	1	Conference		06	
		2	Journals National International		1	

12.	Membership of Professional Bodies	ISTE
13.	Awards if Any	
14.	Any other Achievements	

Annexure :

National/International Conference

- Workshop Conducted/ Attended
- 1 “Three Day Workshop on FPGA for Signal and Image Processing Applications”, at M. S. Engineering College, Bangalore
 - 2) Three Day Faculty Development Program On Research process, Technical paper writing and Patenting, at M. S. Engineering College, Bangalore
 - 3) Two day Faculty Development Program on “EMBEDDED Systems Development” using ARM CORTEX-M3