


Curriculum – Vitae

1.	Name 	Natya.S				
2.	Address and Contact Numbers	Maruthinagar, Yelahanka, Bangalore-64 9731864057				
3.	Date of Birth/Sex/ Marital Status	02-03-1984/ Female/ Unmarried				
4.	Affiliation	VTU				
5.	Educational Qualification	Degree	Year	University /College	Specialization	
		B.E	2006	VTU/ AIT, Chickmangalore	Electronics and Communication Engineering	
		M.E	2010	B.U/ U.V.C.E, Bangalore	Control and Instrumentation	
6.	Experience	Sl. No	College	From	To	Years
		1	MSEC	Aug-2010	Till date	4.5 years
7.	Subject Areas of Interest	Control System, Microcontrollers, Digital Communication				
8.	Research Projects Worked and Grants Received	Nil				
9.	Consultancy Projects Worked	Nil				
10.	Number of Projects/Thesis Supervised • Masters • PhD	Nil				
11.	Publications and Patents (Details can be added in annexure)	1	Conference		01	
		2	Journals National		Nil	
			Journals International		02	
		3	Invited Talks		Nil	
		4	Patents		Nil	
5	Any other		Nil			
12.	Membership of Professional Bodies	Nil				
13.	Awards if Any	Nil				
14.	Any other Achievements	Nil				

International Conference :

- “IMPLEMENTATION OF SECURE POINT TO POINT WIRELESS NETWORK BY INTEGRATING PKDS AND SECRET KEY” at international conference on recent innovations in electrical, electronics, computer and mechanical engineering(ICRIEECME) held at GOA on 13th JULY 2014

Workshop Attended:

1. Two days workshop on “RECENT TRENDS IN POWER ELECTRONICS” organized by department of Electrical Engineering, UVCE under TEQIP on 25th and 26th September-2008
2. One week workshop on “EMBEDDED SOLUTIONS ENGINEERING & LABVIEW” organized by department of Electrical Engineering ,UVCE under TEQIP from 9th to 14th March-2009
3. Three day national level workshop on ”MICROCONTROLLER MSP 430 & ITS APPLICATIONS” held from 30th Jan to 1st Feb-2012 organized by department of Electrical & Electronics Engineering, HKBK college of Engineering
4. Two day faculty development program on “NUVOTON NUMICRO ARM CORTEX-M0 & ITS APPLICATIONS” organized by department of Telecommunication Engineering, Electronics & Communication Engineering and Instrumentation Technology, MSRIT under TEQIP-II on 10th and 11th September-2013

Workshop Attended at MSEC

1. Three day faculty development program on “RESEARCH PROCESS, TECHNICAL PAPER WRITING AND PATENTING” conducted by R&D centre, MSEC-Bengaluru on 22nd to 24th January 2014.
2. Four day faculty development program on “VLSI SYSTEM ON CHIP DESIGN AND VALIDATION” organized by R&D centre and ECE department, MSEC-Bengaluru on 10th to 13th July 2013.
3. Two day workshop on “SPEECH PROCESSING ROBOTICS” conducted by Li2 innovations Pvt.Ltd held at MSEC- Bengaluru on 15th to 16th March 2013.
4. Three day workshop on “FPGA BASED SIGNAL AND IMAGE PROCESSING APPLICATIONS DEVELOPMENT” at MSEC – Bengaluru held on 7th to 9th February 2013.
5. One day workshop based on “ ANDROID BASED APPLICATION DEVELOPMENT” at MSEC – Bengaluru held on 10th November 2012

Invited Talk : -----NIL-----

Journal /Publication

- “SMART COLLEGE BUS TRCKING MANAGEMENT SYSTEM AND ITS APPLICATION” International Journal of Emerging Technologies and Engineering (IJETE), Volume 1 Issue-5, pp 142-147, June 2014
- “IMPLEMENTATION OF SECURE POINT TO POINT WIRELESS NETWORK BY INTEGRATING PKDS AND SECRET KEY” International Journal of Advanced Computational Engineering and Networking(IJACEN) Volume 1 Issue-9, pp 56-61

Patent : -----NIL-----