


Curriculum –Vitae

1.	Name 	Nagayya S Hiremath				
2.	Address and Contact Numbers	#22 2 nd cross 10 th main Attur Layout Yelahanka Bangaluru-64				
3.	Date of Birth/Sex/ Marital Status	01-06-1987/ Male/ Un-Married				
4.	Affiliation	VTU				
5.	Educational Qualification	Degree	Year	University /College	Results	
		B.E	2009	VTU/ REC, Bhalki	67.3%	
		M.Tech	2013	VTU/ RITM, Bangalore	84%	
6.	Experience	Sl. No	College	From	To	Years
		1	MSEC	AUG-2010	Till date	4.5 years
7.	Subject Areas of Interest	Power Electronics, Microprocessor, Basic Electronics				
8.	Research Projects Worked and Grants Received	Nil				
9.	Consultancy Projects Worked	Nil				
10.	Number of Projects/Thesis Supervised • Masters • PhD	Nil				
11.	Publications and Patents (Details can be added in annexure)	1	Conference		NIL	
		2	Journals National		01	
			Journals International		NIL	
		3	Invited Talks		Nil	
		4	Patents		Nil	
5	Any other		Nil			
12.	Membership of Professional Bodies	Nil				
13.	Awards if Any	Nil				

14.	Any other Achievements	Nil
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Annexure :

International Conference :

Workshop Attended:

- Two days workshop on “RECENT TRENDS IN MICROCONTROLLER” organized by department of Electronics Engineering, Reva College of Engineering under TEQIP on 25th and 26th November-2010

Invited Talk : -----NIL-----

Journal /Publication

-----NIL-----

Patent : -----NIL-----